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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/662,809	09/16/2003	Seiji Nagai	T36-159069M/KOH	1114
21254 7	590 05/24/2005		EXAMINER	
MCGINN & GIBB, PLLC			ANDERSON, MATTHEW A	
8321 OLD CO SUITE 200	URTHOUSE ROAD		ART UNIT	PAPER NUMBER
VIENNA, VA	22182-3817		1722	
,			DATE MAIL ED. 05/04/2004	-

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	v v
	10/662,809	NAGAI ET AL.	
Office Action Summary	Examiner	Art Unit	
·	Matthew A. Anderson	1765	
 The MAILING DATE of this communication appearing for Reply 	ppears on the cover sheet wit	h the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repleted in the period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statur Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a re ply within the statutory minimum of thirty I will apply and will expire SIX (6) MONT te, cause the application to become ABA	ply be timely filed (30) days will be considered timely. HS from the mailing date of this communication NDONED (35 U.S.C. § 133).	1.
Status			
 1) ⊠ Responsive to communication(s) filed on 16 s 2a) ☐ This action is FINAL. 2b) ⊠ This 3) ☐ Since this application is in condition for allows closed in accordance with the practice under 	is action is non-final. ance except for formal matte	•	3
Disposition of Claims			
4) Claim(s) <u>1-8</u> is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) <u>1-3 and 5-8</u> is/are rejected. 7) Claim(s) <u>4</u> is/are objected to. 8) Claim(s) are subject to restriction and/	awn from consideration.		
Application Papers			
9) The specification is objected to by the Examin 10) The drawing(s) filed on 1/08/2004 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	accepted or b) objected e drawing(s) be held in abeyand ction is required if the drawing(s)	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(o	d).
Priority under 35 U.S.C. § 119			
a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority documer application from the International Burea * See the attached detailed Office action for a list	nts have been received. Its have been received in Aporty documents have been and (PCT Rule 17.2(a)).	oplication No received in this National Stage	
Attachment(s)			•
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 9/16/03;9/22/04.	Paper No(s)	ummary (PTO-413) /Mail Date formal Patent Application (PTO-152) 	

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DETAILED ACTION

Claim Rejections - 35 USC § 112

- The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claim 8 recites the limitation "... the second layer forming step..." in lines 12 and 13. There is insufficient antecedent basis for this limitation in the claim. Claim 8 is independent and has not reference to a second layer forming step.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

5. Claims 1-3, 5-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shibata et al. (US 6,824,610 B2).

Shibata et al. discloses a process for producing nitride crystal substrates for use in making light emitting devices (see Fig. 5). In Example 9, one embodiment is detailed. A silicon substrate was provided and a silver film was applied thereto as a buffer layer. A further buffer of AIN was applied. Then, a layer of GaN was grown on the AIN surface using MOCVD. After, the substrate was transferred to a HVPE furnace where a thick layer of GaN (i.e. the first layer) was deposited on the previous layer. Then the silver film was subjected to etching thus removing both the silver and the silicon substrate. Alternative materials were given in cols. 17 and 18 to include AlGaN and GalnN as the substrate prepared.

Shibata et al. does not disclose explicitly to remove almost all of the silicon substrate by etching.

However, in respect to claims 1 and 7, it would have been obvious to one of ordinary skill in the art at the time of the present invention to produce a Group III-nitride compound semiconductor substrate (including Al containing nitrides) by HVPE on a buffered silicon substrate and then remove the silicon substrate from a rear surface by etching after the completion of the first layer forming step because such was suggested by Example 9 and col. 18 of Shibata et al. Almost the whole Si substrate is left since only the AIN/GaN buffer layer is left after the etching.

In respect to claim 2, it would have been obvious to one of ordinary skill in the art at the time of the present invention to perform a second layer forming step after the Si

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substrate removing step because Shibata et al. describes in Col. 18 lines 22-30 the use of the substrate in forming laser diodes which require further nitride film formation.

(See Fig. 5)

In respect to claim 3, it would have been obvious to one of ordinary skill in the art at the time of the present invention to remove a large part of said first Group III nitride compound semiconductor layer from the rear surface by etching after the completion of the second layer forming step because Shibata et al. suggests removing, in example 1 col. 8 lines 15-25, the rear of the substrate which has AIN thereon.

In respect to claims 5 and 6, it would have been obvious to one of ordinary skill in the art at the time of the present invention to optimize the result dependent process parameters of temperature and film thickness because these parameters where described throughout Shibata et al. and the applicant has provides no proof that these were critical.

Allowable Subject Matter

- 6. Claim 4 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 7. The following is a statement of reasons for the indication of allowable subject matter:

The closest prior art of Shibata et al. does not suggest forming an etch stop layer containing a larger amount of aluminum than an amount of aluminum contained in each of the first and second Group III-nitride compound semiconductor layers on the nitride

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substrate before the second layer forming step where the first layer removing step completely removes the first Group III nitride compound semiconductor layer.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew A. Anderson whose telephone number is (571) 272-1459. The examiner can normally be reached on M-F, 8:30-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Benjamin Utech can be reached on (571) 272-1137. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MAA May 19, 2005 BENJAMIN L. UTECH
SUPERVISORY PATERY DISAMINER
TECHNOLOGY CENTER 1700